

FIG. 1

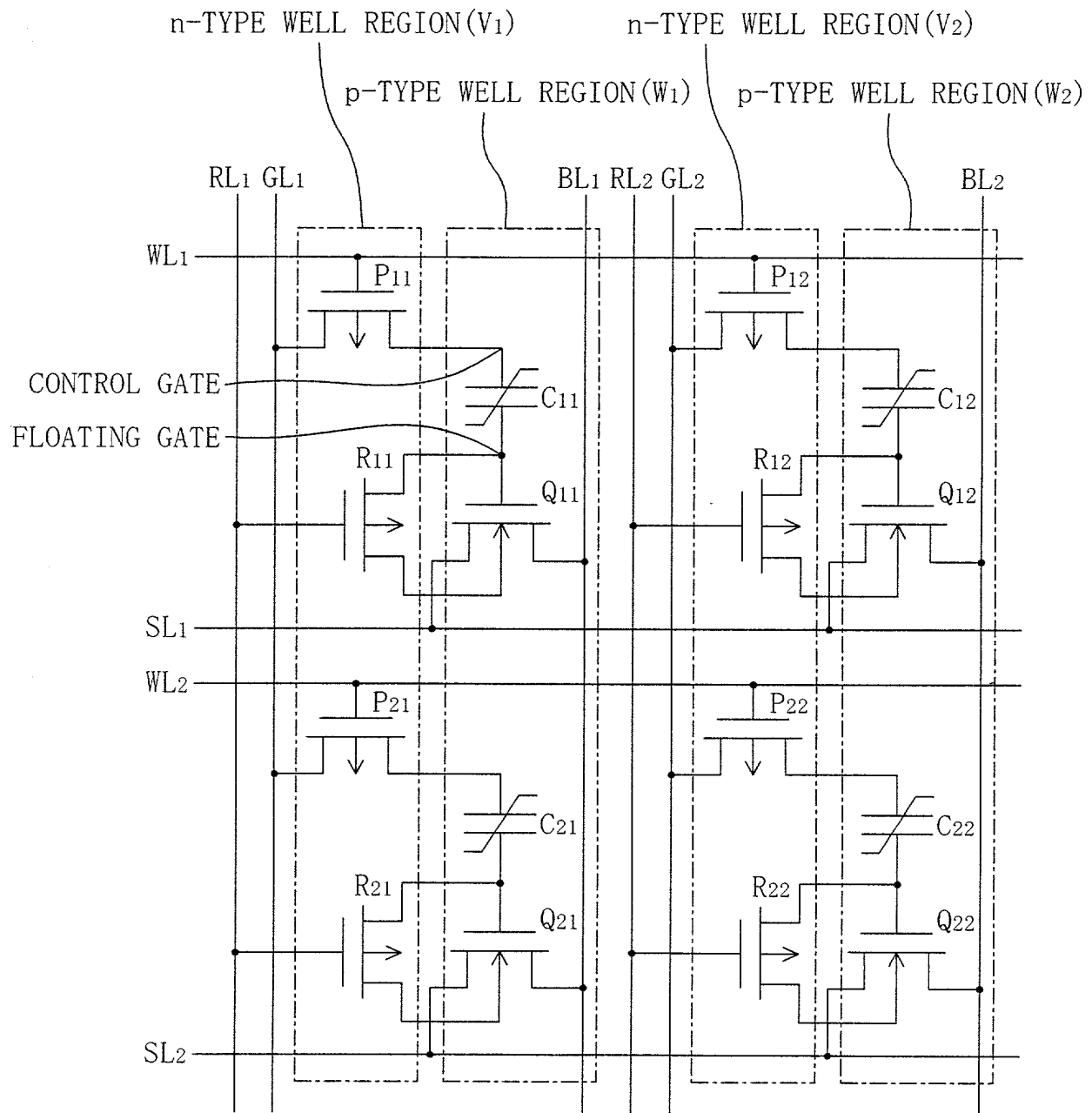


FIG. 2

	OPERATION	
	WRITE	READ
WL1	0	0
SL1	0	0
GL1	$\pm V_p$	V_r
BL1	0	V_d
RL1	0	V_p
V1	V_p	V_p
W1	0	0
WL2	V_p	V_p
SL2	0	0
GL2	0	0
BL2	0	0
RL2	V_p	V_p
V2	V_p	V_p
W2	0	0

FIG. 3

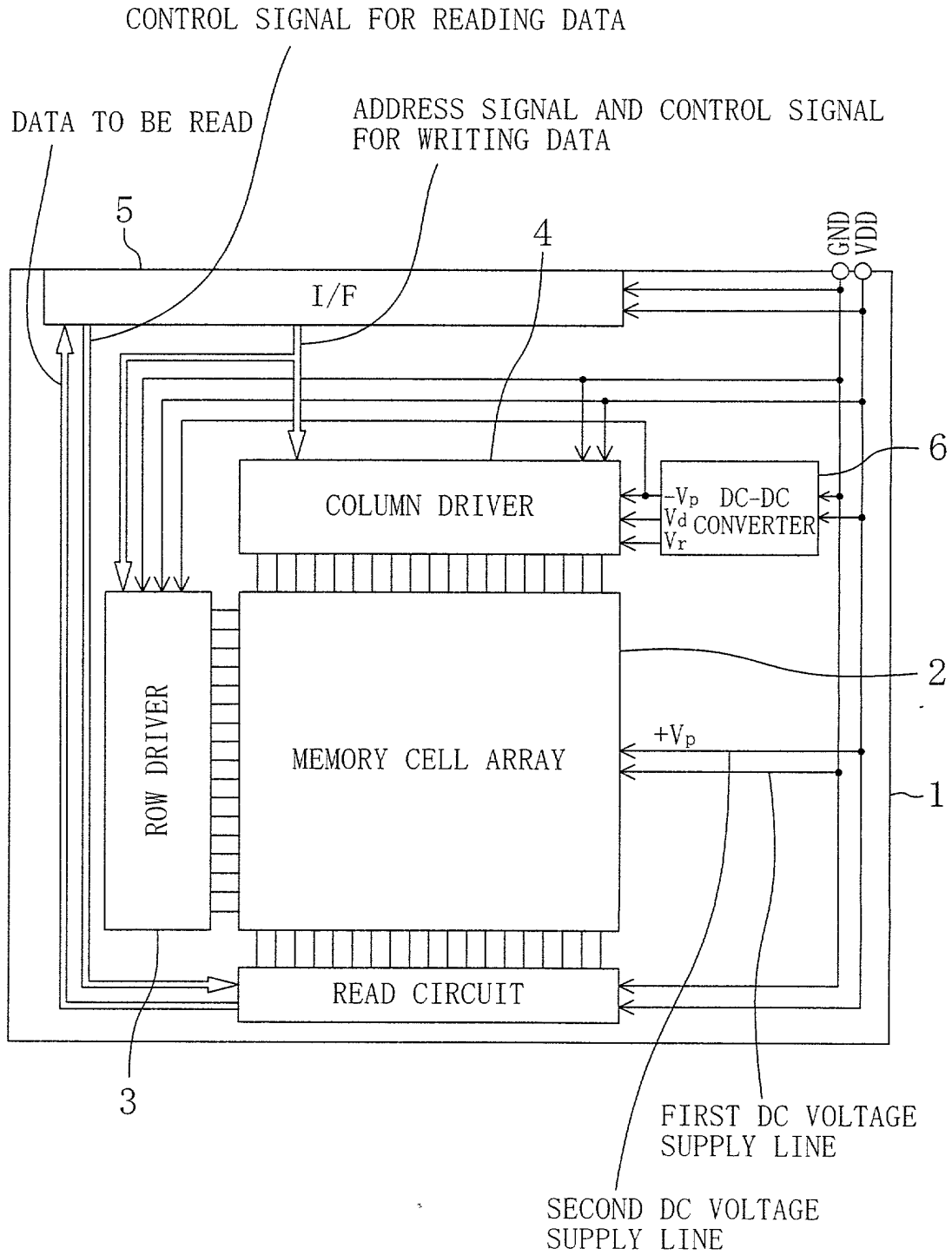


FIG. 4

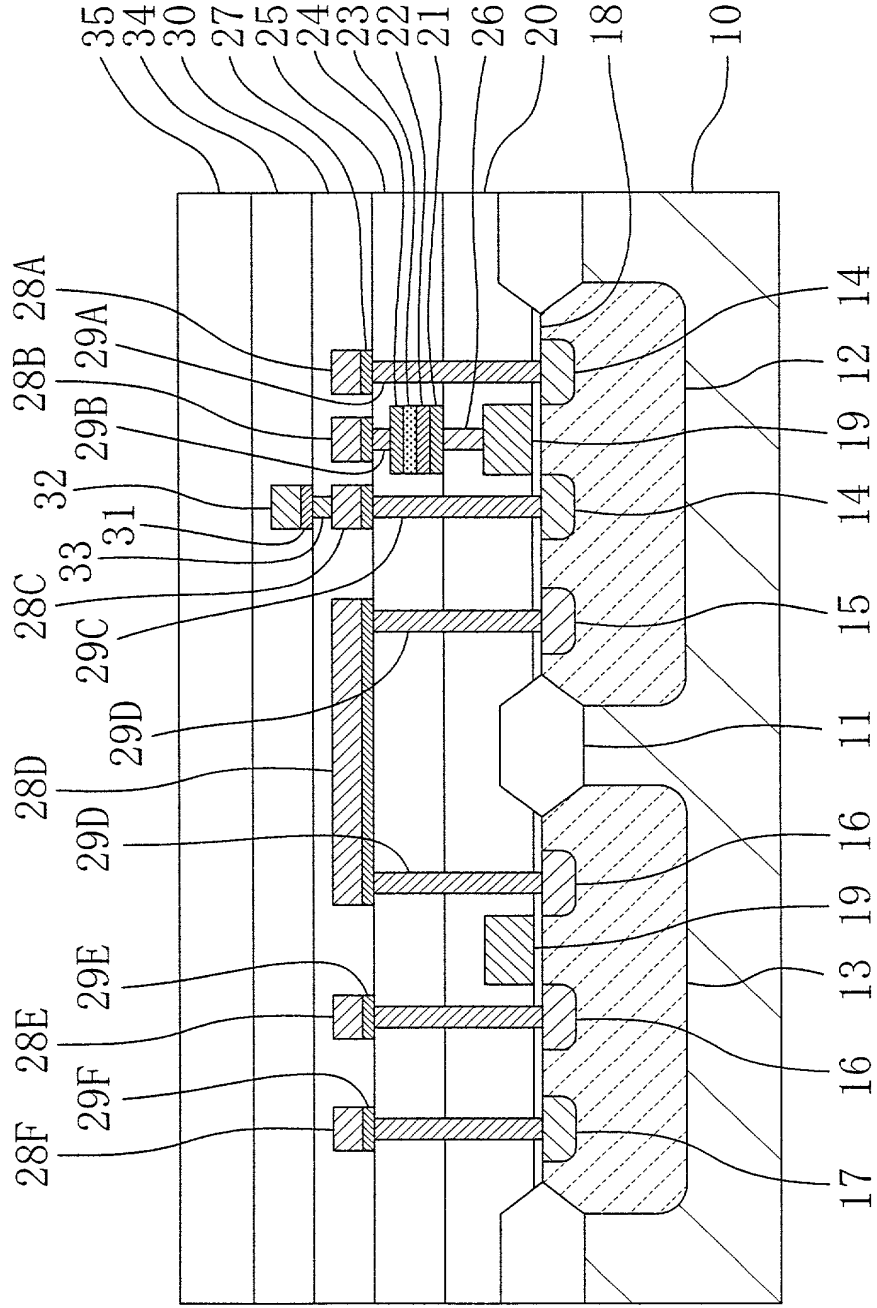


FIG. 5A
PRIOR ART

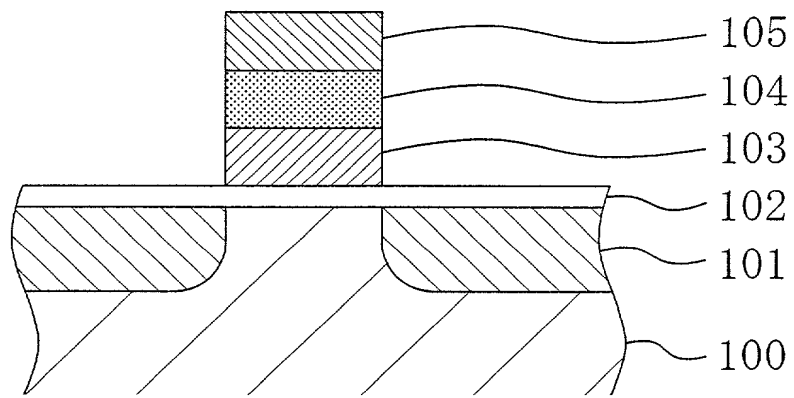


FIG. 5B
PRIOR ART

